

Abstracts

Parallel Processing Application to Nonlinear Microwave Network Design (Comments)

B.R. Sperber. "Parallel Processing Application to Nonlinear Microwave Network Design (Comments)." 1991 *Transactions on Microwave Theory and Techniques* 39.2 (Feb. 1991 [T-MTT]): 371-372.

In the above paper, Fig. 6 shows a speedup curve for a network of Transputer processors and apparently is a plot of the data given there in Table I. Could the authors please explain how the data shown support their summarized result in Section IX, where they state that "the addition of more Transputers will also reduce the computational time."

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